

Docket No. 10001.000700 (NVLS 327)
Response To Office Action
March 29, 2004



9/A
C.B.—
4/22/04

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Jaideep Jain, et al.

Application No.: 09/677,087

Examiner: Lee, Douglas S

Filing Date: September 29, 2000

Art Unit: 2125

Title: Operational Lists for Simultaneous Wafer Scheduling and System Event
Scheduling

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Technology Center 2100

RESPONSE TO OFFICE ACTION

INTRODUCTORY COMMENTS

Sir:

This paper is responsive to the office action mailed on December 31, 2003
(hereinafter the "last office action").

Please amend the above-identified application as follows: